

Figure 1

Tek Stop Single Seq 5.00KS/s

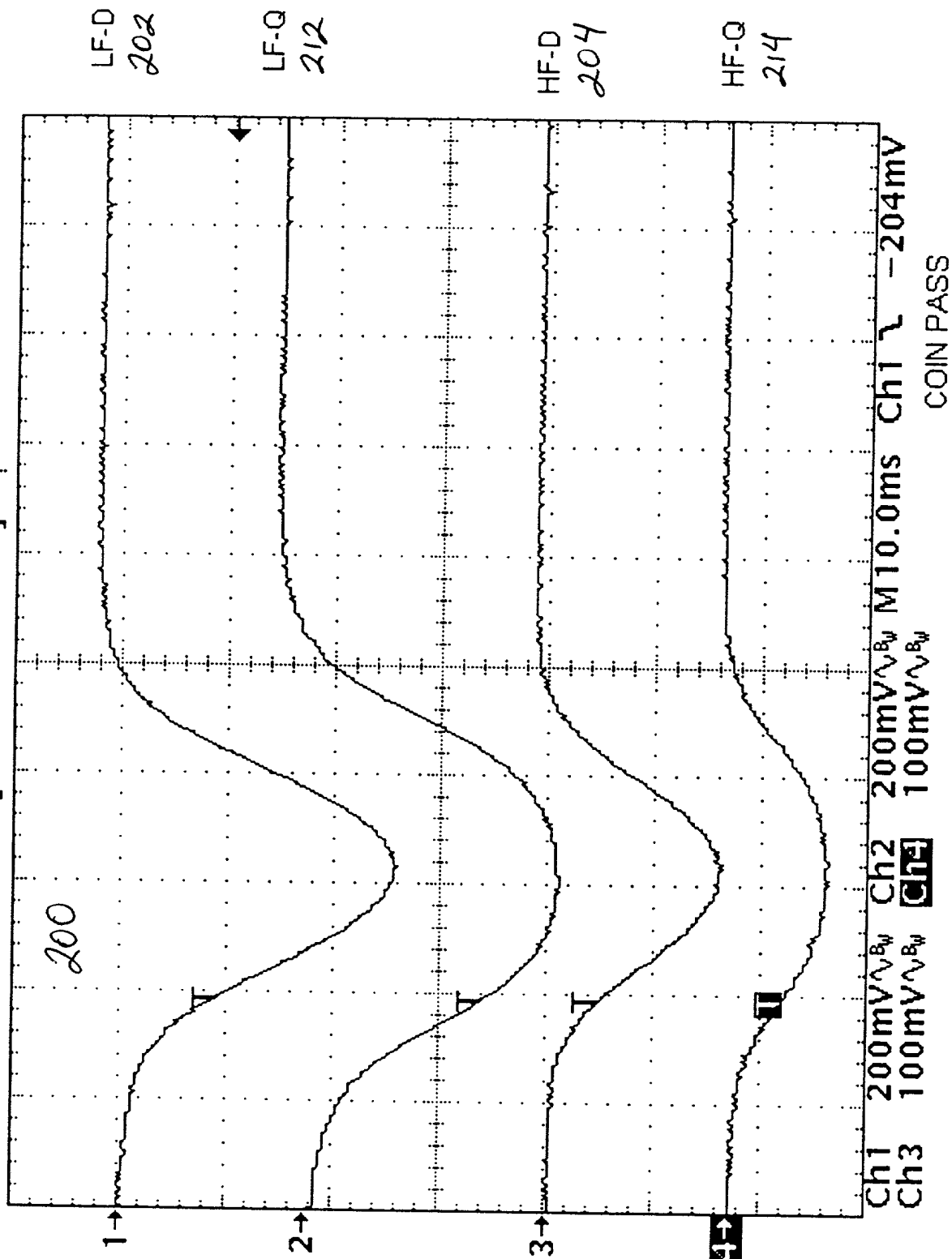


FIGURE 2

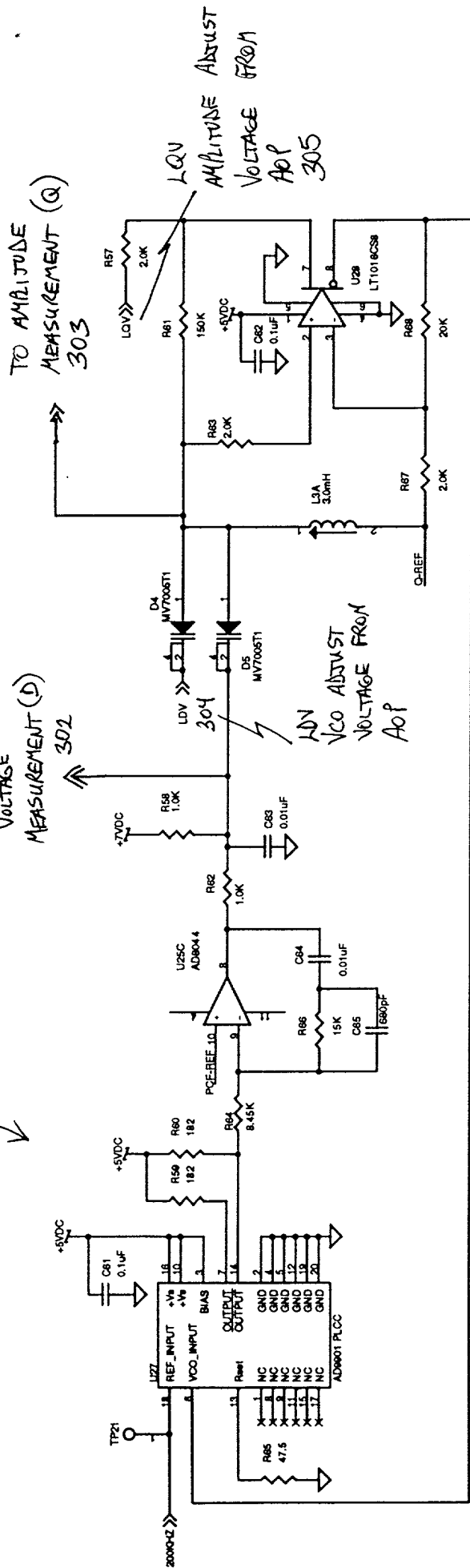
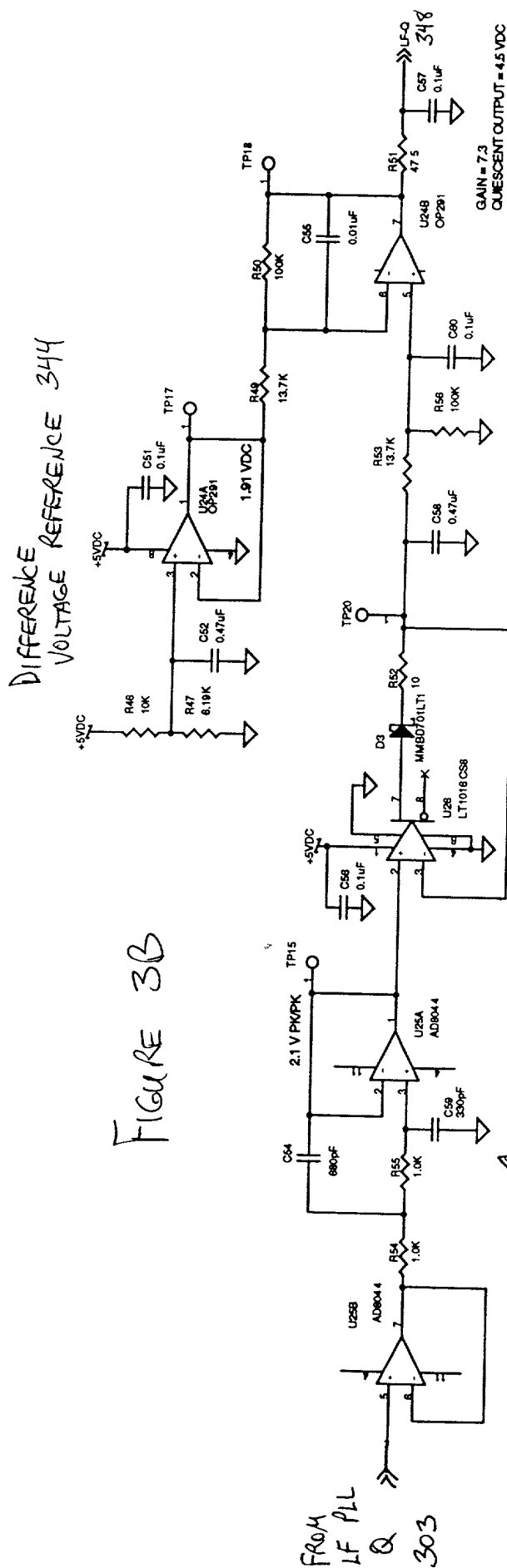
[illegible]

Figure 3A



112

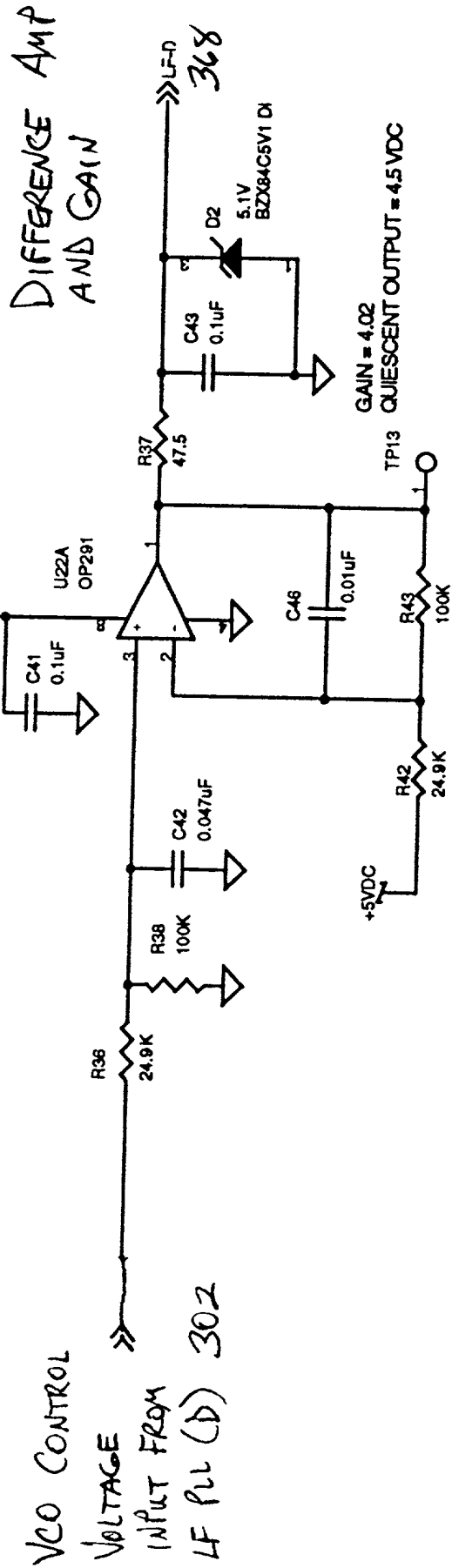
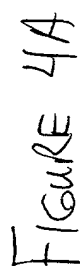


FIGURE 3C



DIFFERENCE  
VOLTAGE REFERENCE 444

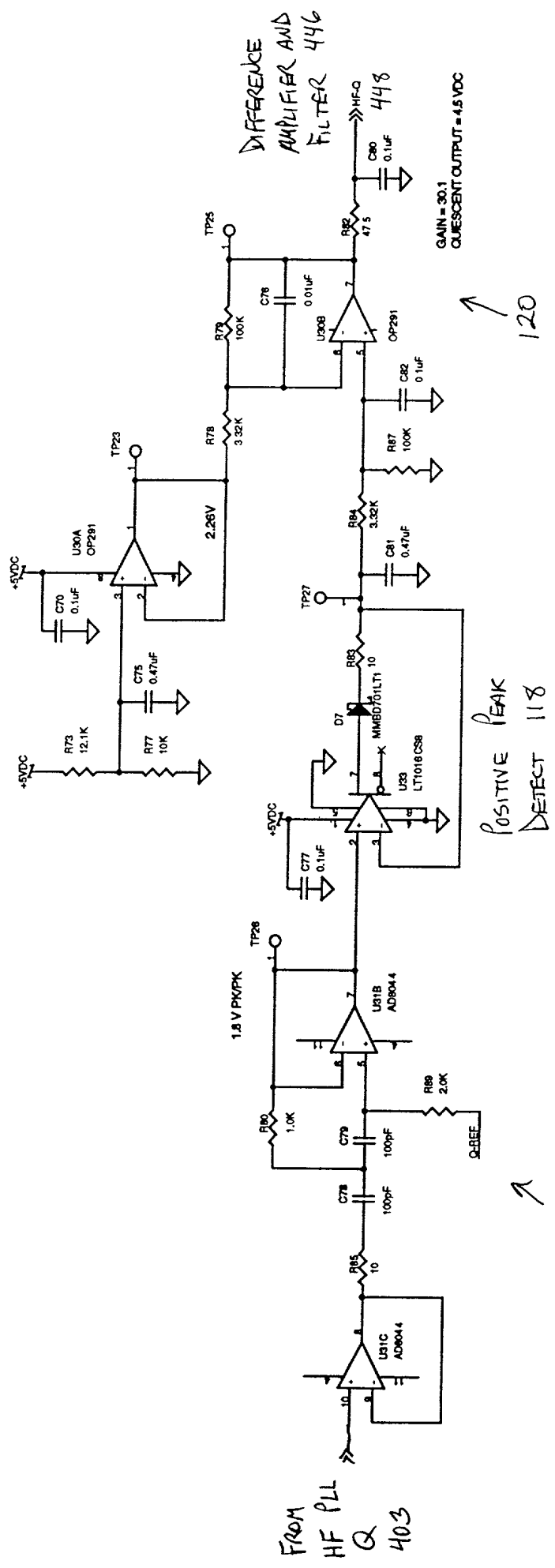
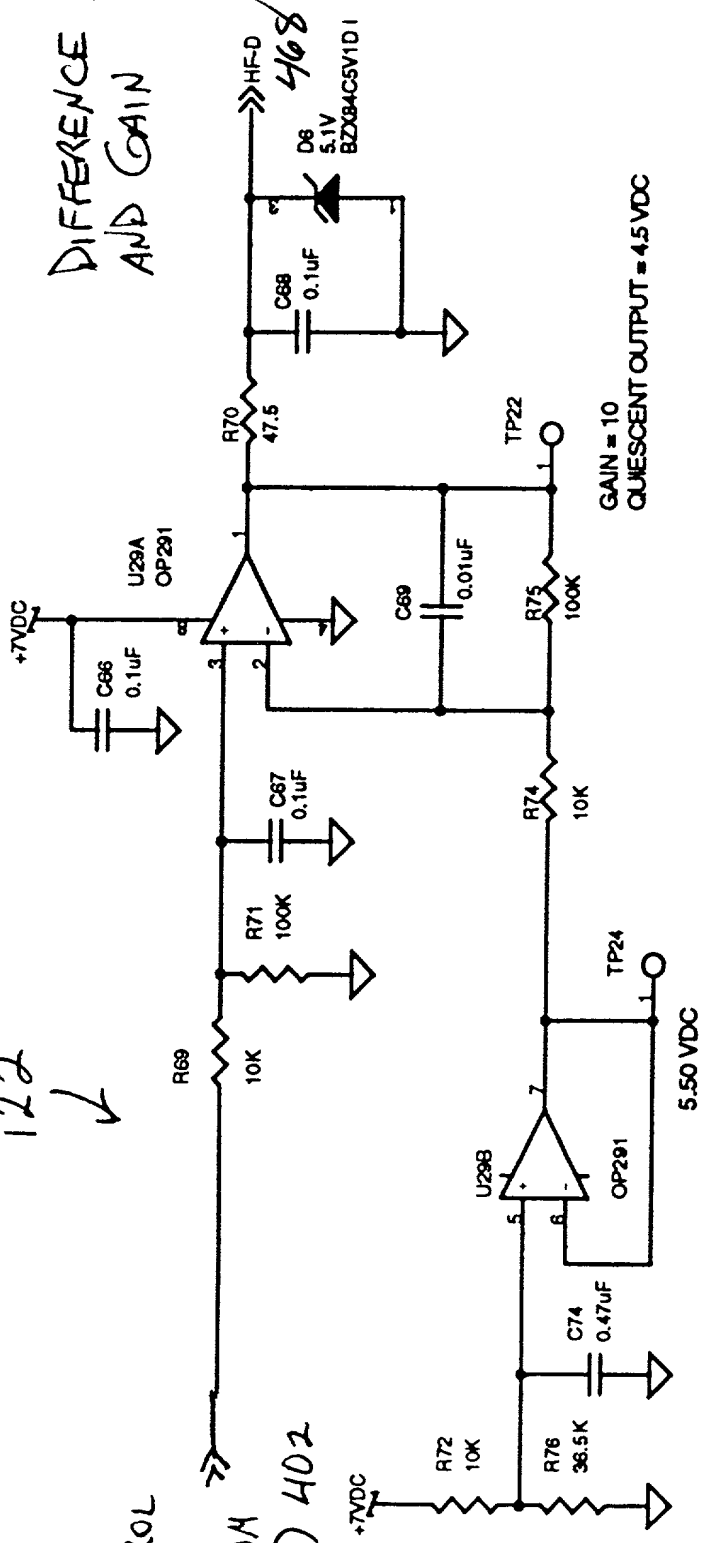


FIGURE 4b

122

DIFFERENCE AMP  
AND GAIN

VCO CONTROL  
VOLTAGE  
INPUT FROM  
HF PLL (D) 402

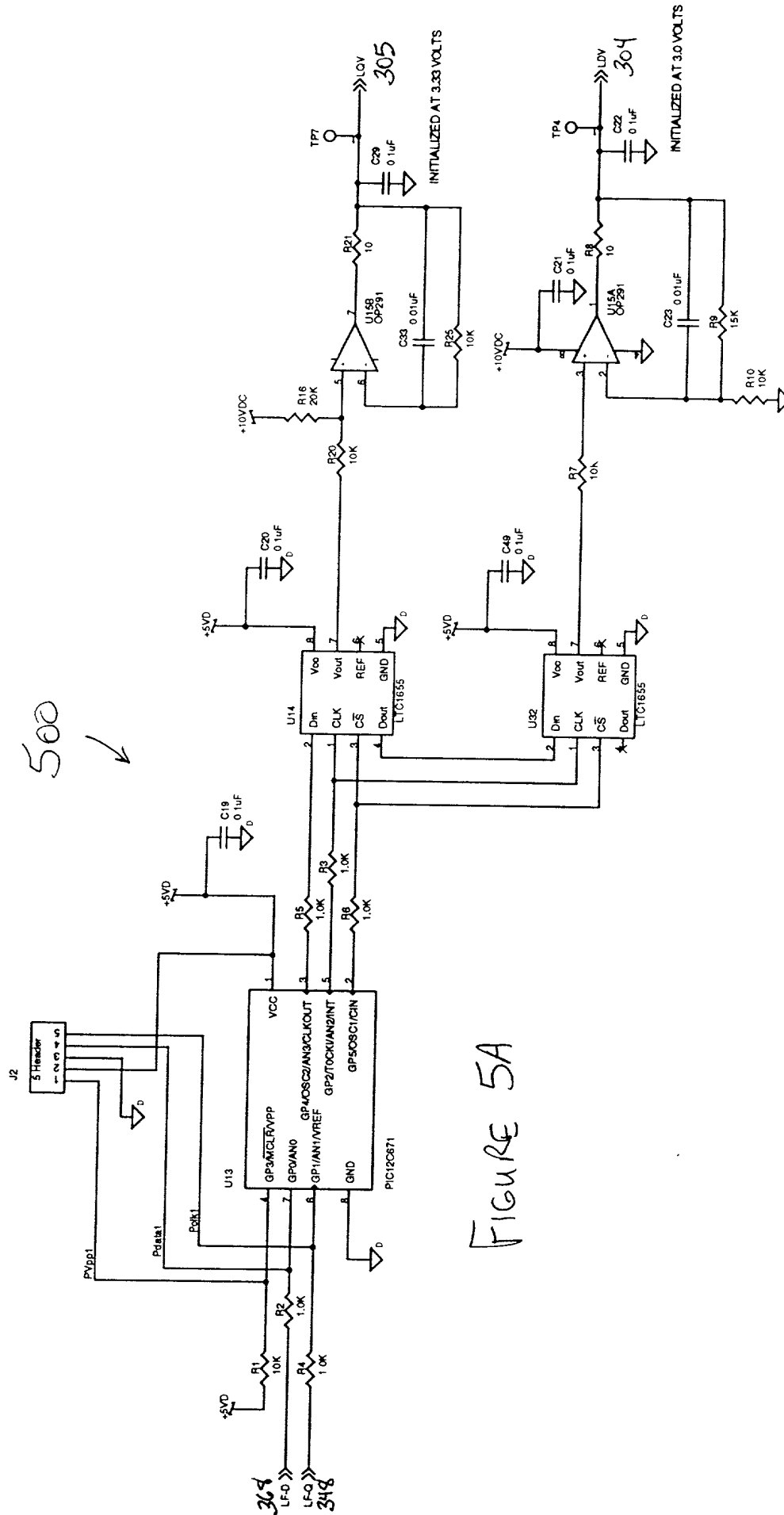


GAIN = 10  
QUIESCENT OUTPUT = 4.5 VDC

FIGURE 4C



500 →



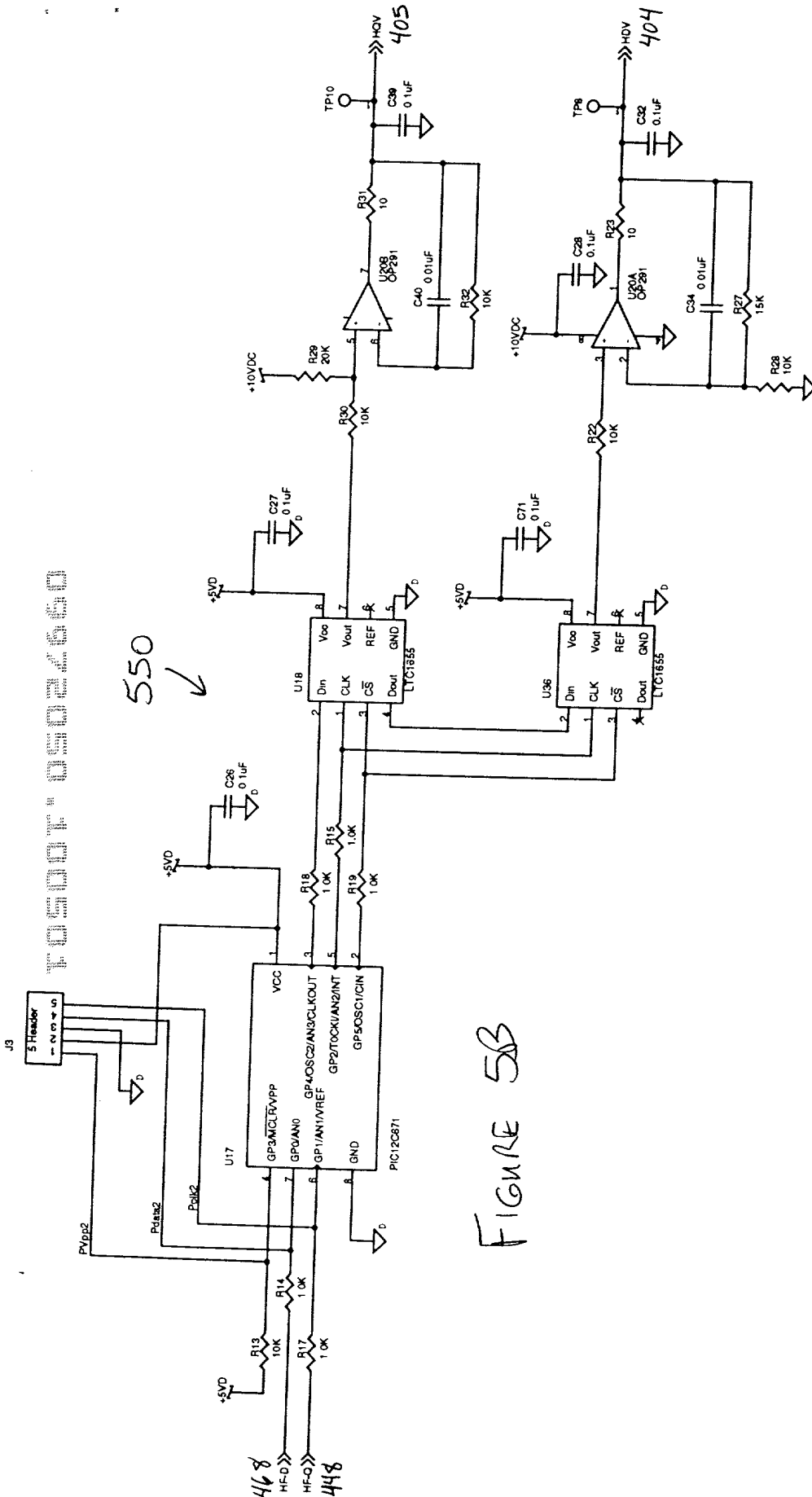
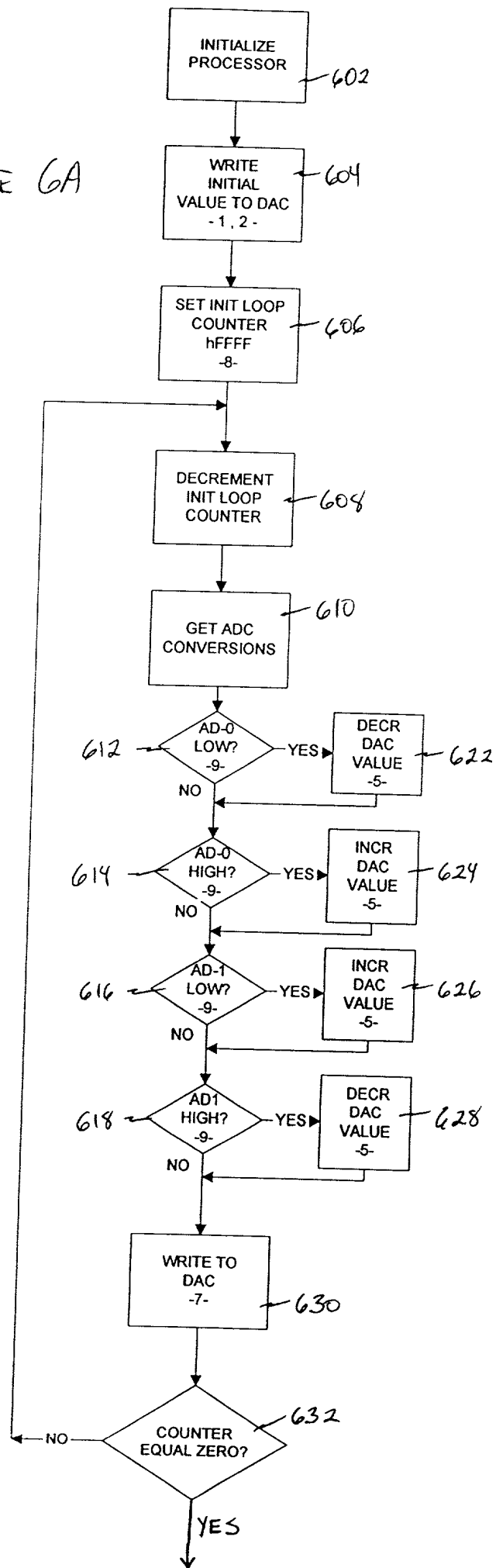


FIGURE 5B

FIGURE 6A



0992050-10904  
T05001-0502650

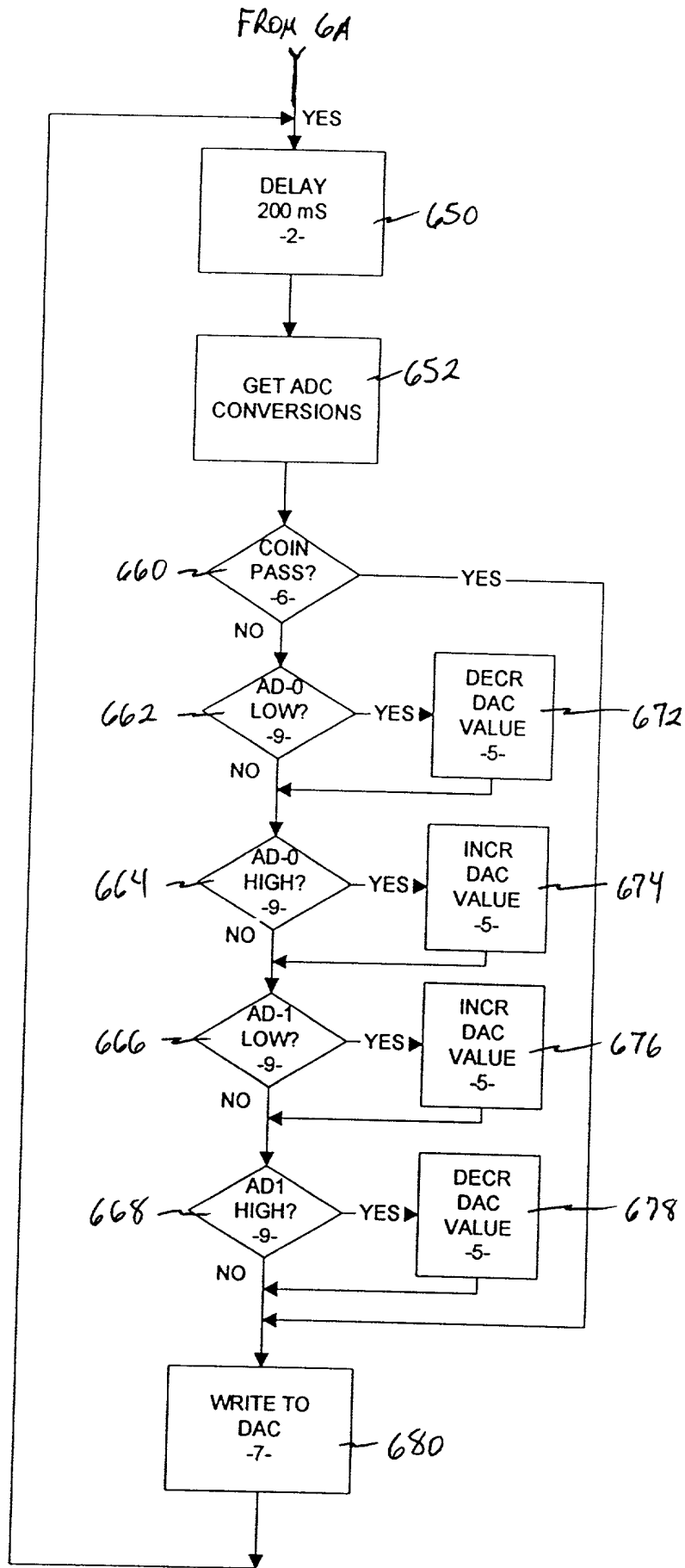


FIGURE 6B

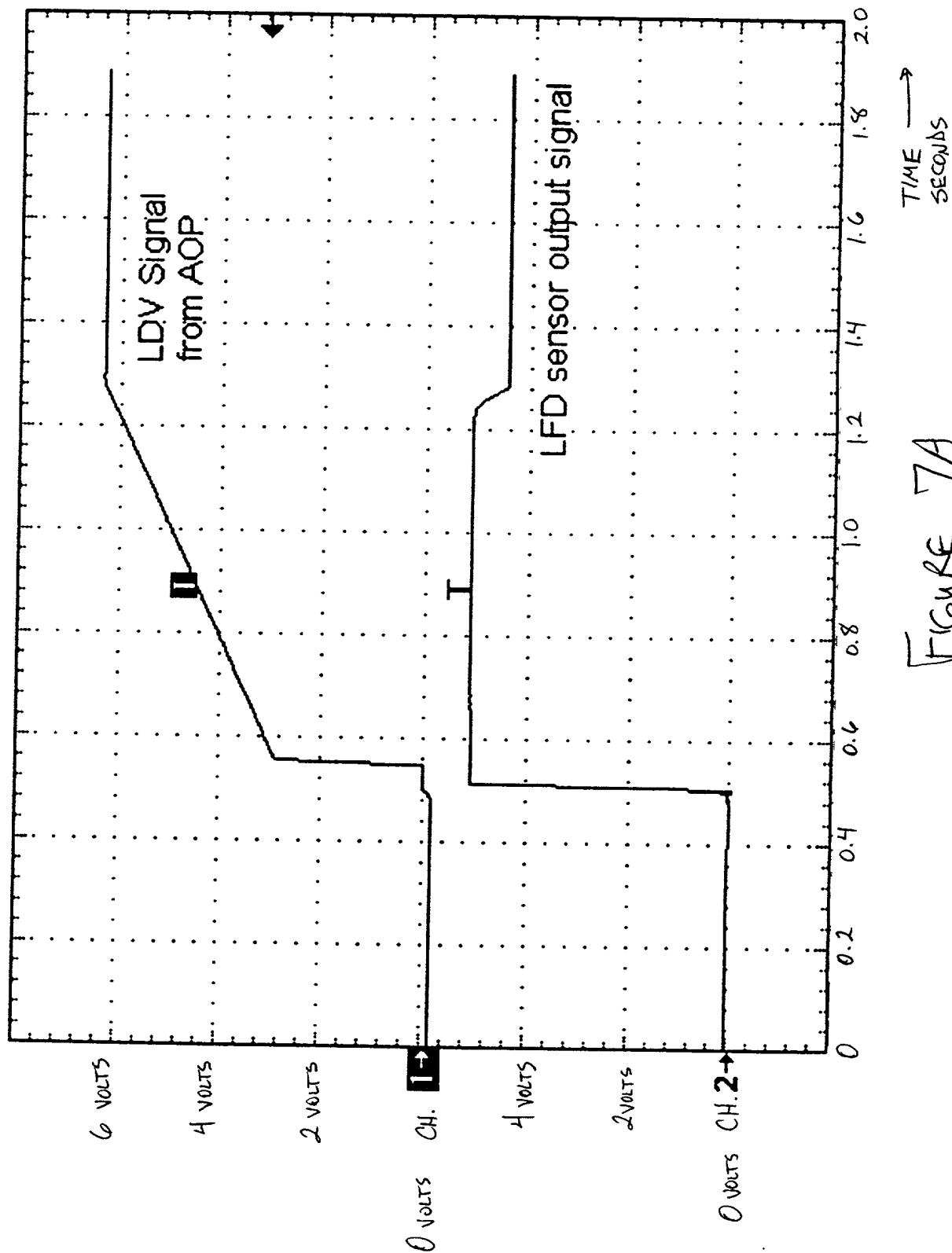


FIGURE 7A

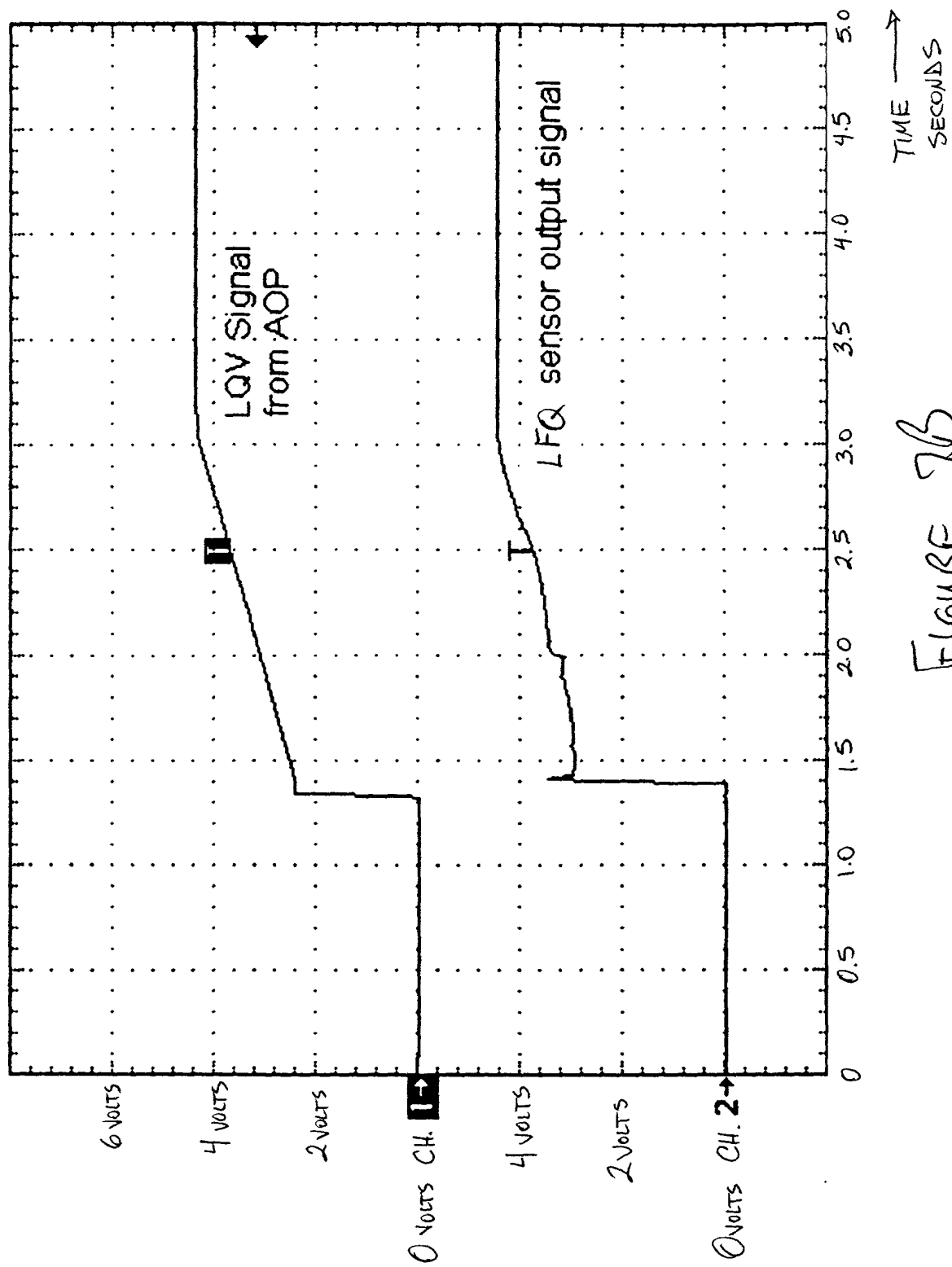
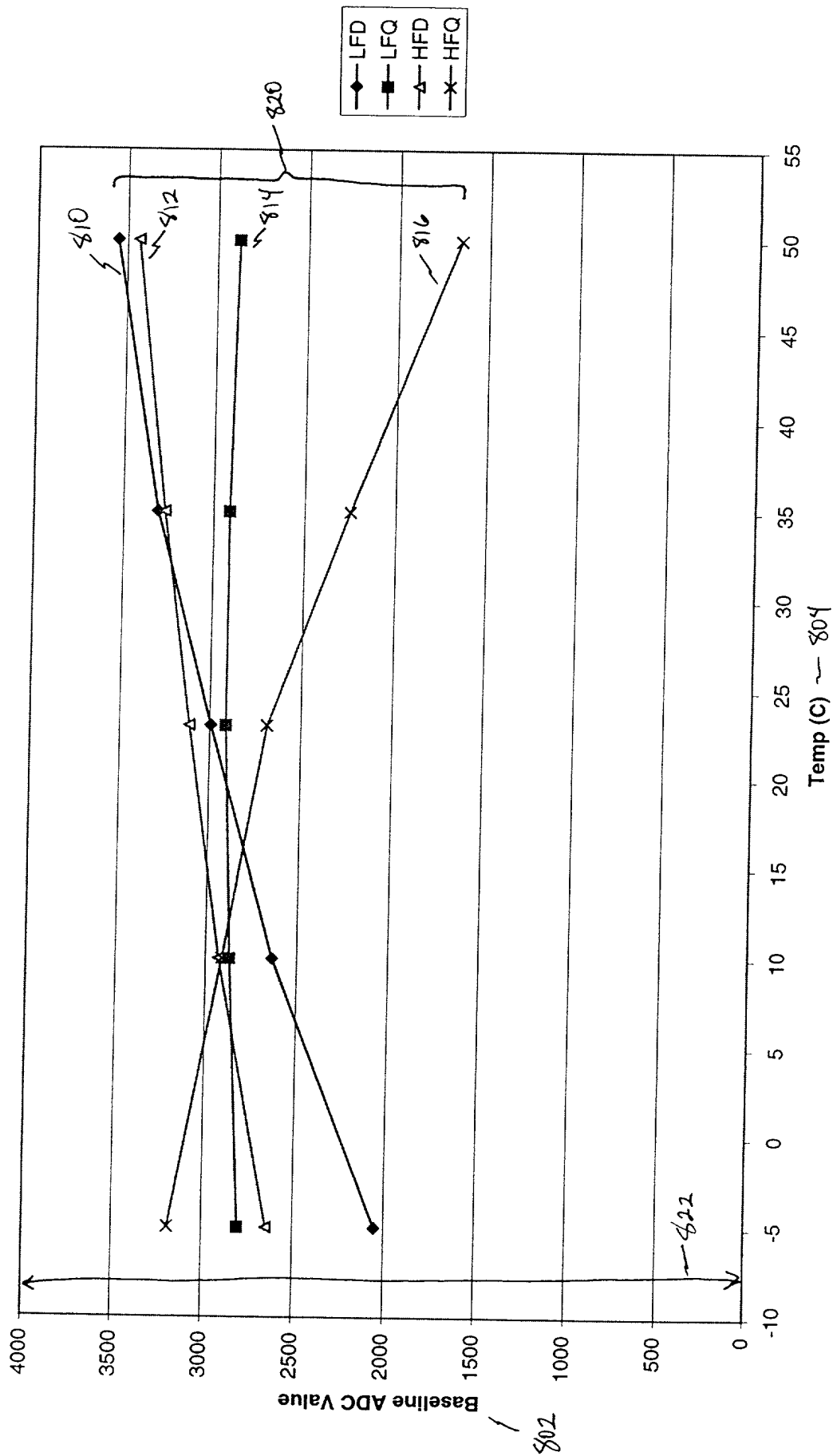


FIGURE 7B

# FIGURE 8 (PRIOR ART)

Baseline Vs. Temp



Baseline Response Vs. Temp

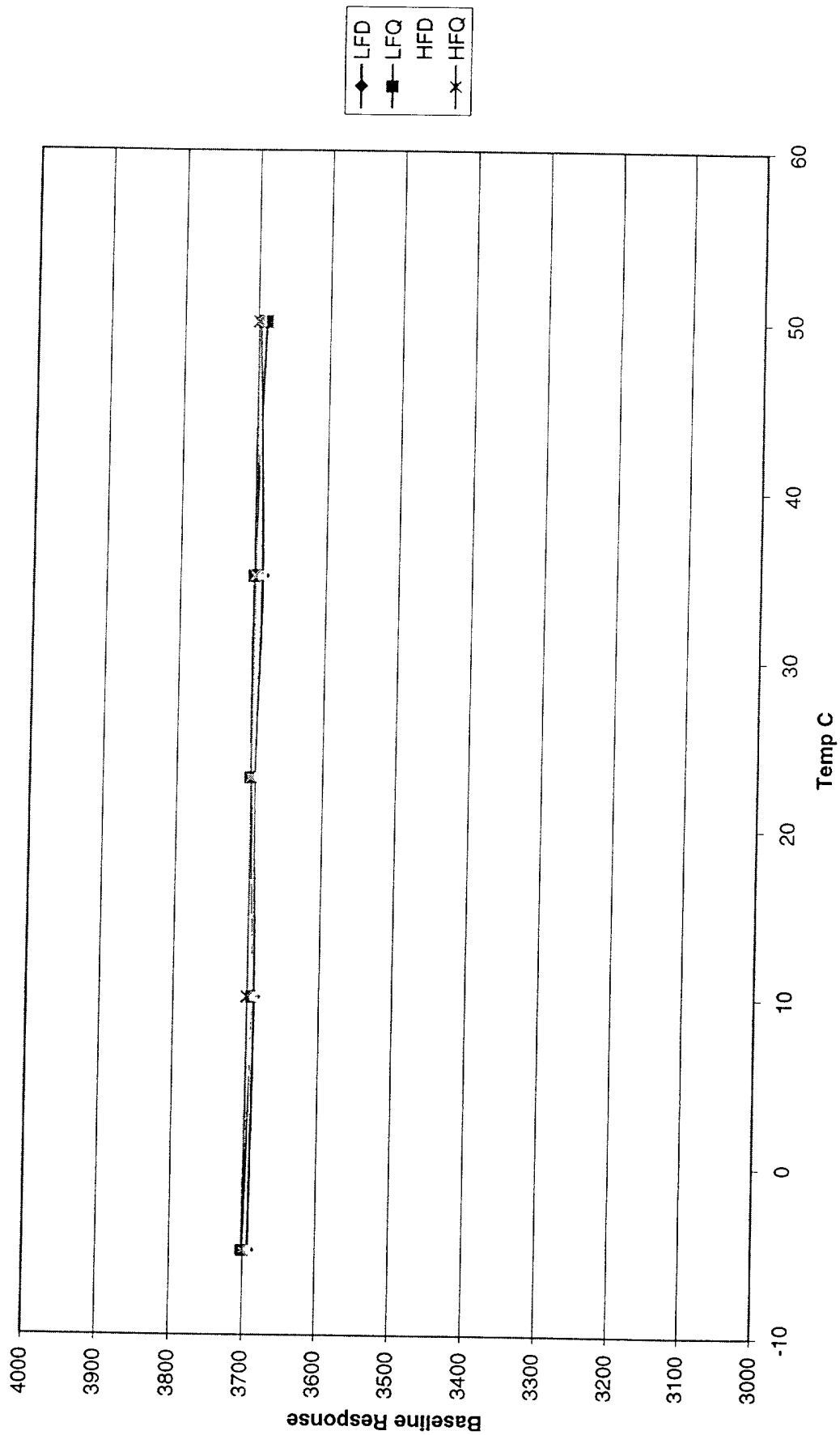


Figure 9